

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,123,301 B1
APPLICATION NO. : 09/579646
DATED : October 17, 2006
INVENTOR(S) : Katsufumi Nakamura et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 5, lines 40-41, "circuit pixel gain amplifier." should read -- pixel gain amplifier circuit. --

Col. 5, line 49, "circuit pixel gain amplifier." should read -- pixel gain amplifier circuit. --

Col. 6, line 9, "circuit pixel gain amplifier," should read -- pixel gain amplifier circuit, --

Col. 6, line 36, "circuit pixel gain amplifier." should read -- pixel gain amplifier circuit. --

Signed and Sealed this

Sixth Day of March, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office